



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Gordon R. Mcleod

Serial No.: 10/626,780

Filed: July 24, 2003

For: Formatting Data For A Buffer

§ Group Art Unit: Unknown

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Examiner: Unknown

Atty. Dkt. No.: ITL.1001US (P16573)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

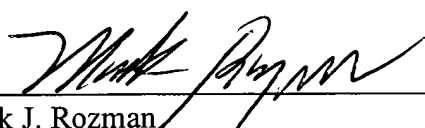
Applicant submits the references listed on the attached form PTO 1449, copies of the references are enclosed.

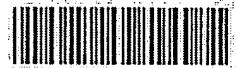
This statement is being filed before the receipt of a first Office action on the merits.

Please apply any charges or credits to Deposit Account 20-1504.

Respectfully submitted,

Date: January 26, 2004


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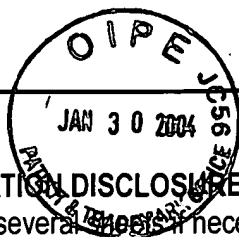


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PATENT TRADEMARK OFFICE

Date of Deposit: January 26, 2004
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Jennifer Juarez



INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

ATTY DOCKET NO.
ITL1001US (P16573)

SERIAL NO.
10/626,780

APPLICANT(S):
GORDON R. MCLEOD

FILING DATE:
July 24, 2003

GROUP ART UNIT:
Unknown

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.						
	B.						
	C.						
	D.						
	E.						
	F.						
	G.						
	H.						
	I.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J.							
	K.							
	L.							
	M.							
	N.							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	O.	Intel® IXF1010 10-port 100/1000 Mbps Ethernet Media Access Controller With System Packet Interface Level 4 Phase 2 (SP14-2), Product Description. Intel Corporation 2002. developer.intel.com.
	P.	Intel® Common System-Level Interface Connecting 10 GbE LANs/WANs to OC-192 MANs, White Paper. August 30, 2002.
	Q.	SPI-4 Phase 2 Performance in Gigabit Ethernet Media Access Controllers, Application Note. Intel Corporation. April 2002.
	R.	IXF18101 10 Gbps Physical Layer Device. Intel® Developer. http://www.intel.com/design/network/products/optical/framers/ixf18101.htm . Accessed June 16, 2003.
	S.	
	T.	
	U.	

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.